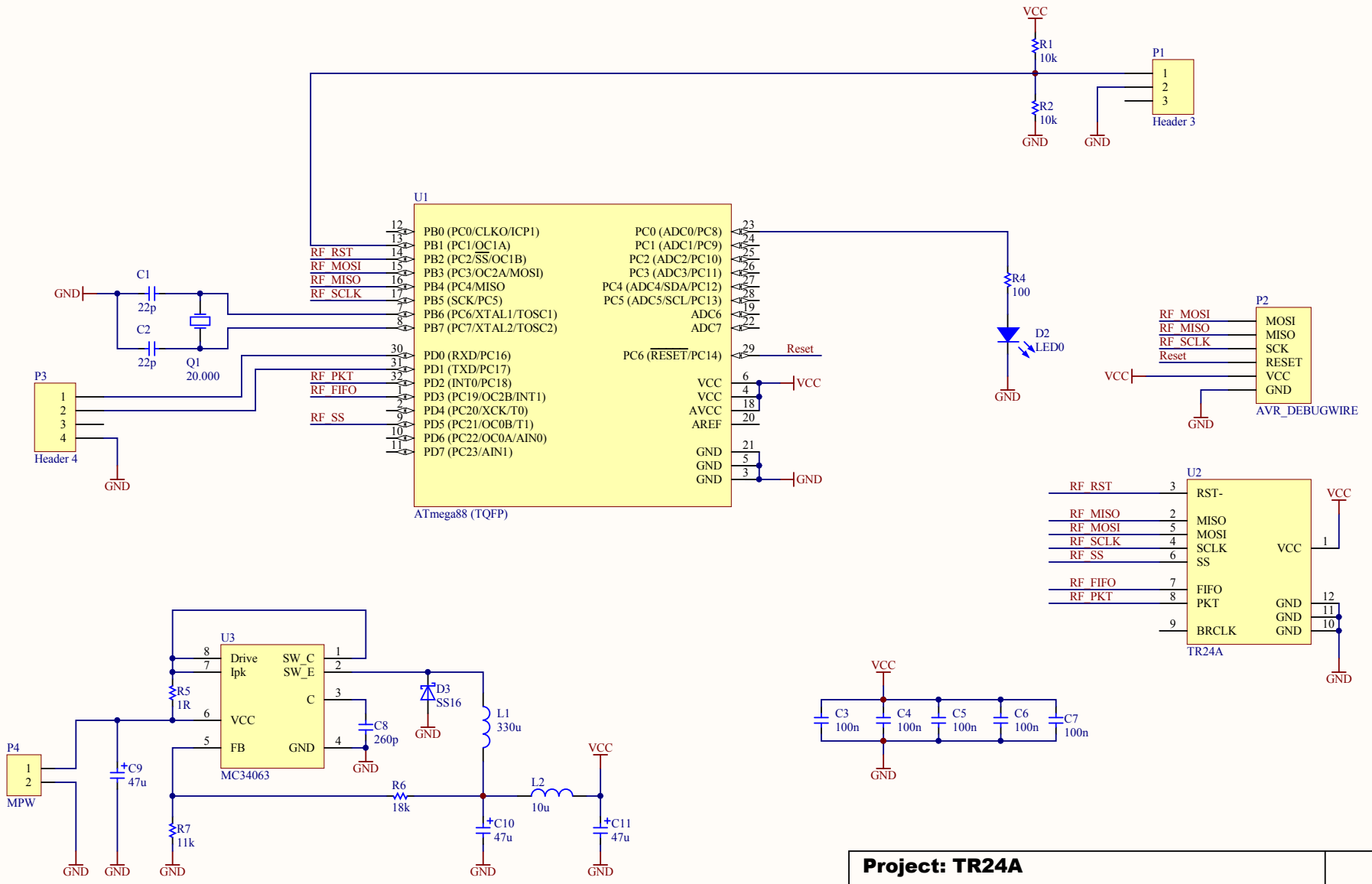
VCC
R1
10k
R2
10k
P1
Header 3
GND
U1
ATmega88 (TQFP)
PB0 (PC0/CLKO/ICP1)
PB1 (PC1/OC1A)
PB2 (PC2/SS/OC1B)
PB3 (PC3/OC2A/MOSI)
PB4 (PC4/MISO)
PB5 (SCK/PC5)
PB6 (PC6/XTAL1/TOSC1)
PB7 (PC7/XTAL2/TOSC2)
PD0 (RXD/PC16)
PD1 (TXD/PC17)
PD2 (INT0/PC18)
PD3 (PC19/OC2B/INT1)
PD4 (PC20/XCK/T0)
PD5 (PC21/OC0B/T1)
PD6 (PC22/OC0A/AIN0)
PD7 (PC23/AIN1)
PC0 (ADC0/PC8)
PC1 (ADC1/PC9)
PC2 (ADC2/PC10)
PC3 (ADC3/PC11)
PC4 (ADC4/SDA/PC12)
PC5 (ADC5/SCL/PC13)
ADC6
ADC7
PC6 (RESET/PC14)
VCC
AVCC
AREF
GND
RF_RST
RF_MOSI
RF_MISO
RF_SCLK
RF_PKT
RF_FIFO
RF_SS
Reset
C1
22p
C2
22p
Q1
20.000
P3
Header 4
R4
100
D2
LED0
P2
AVR_DEBUGWIRE
MOSI
MISO
SCK
RESET
U2
TR24A
RST-
SCLK
SS
FIFO
PKT
BRCLK
U3
MC34063
Drive
Ipk
SW_C
SW_E
C
VCC
FB
GND
R5
1R
R6
18k
R7
11k
C8
260p
D3
SS16
L1
330u
L2
10u
C9
47u
C10
47u
C11
47u
P4
MPW
C3
100n
C4
100n
C5
100n
C6
100n
C7
100n
Project: TR24A
Sheet: *
Sheet of
Date: 10/2/2009
Time: 6:10:50 PM
BSVi